



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TAKITA, et al.

Art Unit: 2823

Application No.: 09/046,671

Examiner: K. EATON

Filed: March 24, 1998

Attorney Dkt. No.: 108077-08003

For: SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING THE SAME

Commissioner for Patents  
Washington, D.C. 20231

August 12, 2002

**AMENDMENT**

Sir:

Prior to initial examination of the application, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Three times amended) A semiconductor device comprising:  
a lightly doped semiconductor substrate of a first conduction type;  
a buried semiconductor layer of a second conduction type formed in a first region of the semiconductor substrate, spaced from a surface of the semiconductor substrate;  
a semiconductor region of the second conduction type extending from the surface of the semiconductor substrate to a peripheral portion of the buried semiconductor layer; and